MICROPROCESSORS AND MICROSYSTEMS

Index to Volume 14 Numbers 1–10, pages 1–680 (1990)

Subject index

Abstract data types 153, 161
Adaptive equalizers 31
Apple Macintosh 167
ASICs 17, 219, 240, 256, 291, 341, 531
Assembler 177
Automotive applications 318

Binary-decision machines 231
Bus interfacing 306

Cache address comparators 653
Cache coherence 491
Cache memory 47, 61, 653, 664
Cache tag RAMs 47, 61, 664
CAD 17
Cellprocessors 119
Command language 517
Computer vision 573
Control applications 3, 219, 231
Custom microelectronics 197

Data communications 31
Data compression 555
Database machines 351
Decoders 256
Digital communications 97, 211, 256
Digital control 219, 231
Digital filters 31, 107, 555
Digital satellite modem 97
Digital signal processing 97, 497, 511, 555, 583
Digital signal processors
  TMS32020 511
  TMS320C25 97, 511
  Zoran Vector Signal Processor 555
Digital sound broadcasting 531
Distributed software 10

Embedded systems 171, 435
Emulation 291

Fault tolerance 599
FIFO links 297
Formal methods 318, 623, 631
Futurebus 491

Gate arrays 205, 240, 245, 256, 531
Graphics server 83

Hypercube multiprocessors 101

Image generation 341
Image processing 107, 267, 573, 583
Independent actuator 517
Inmos links 301, 644
Instruction sets 341, 631
Integrated sensors 74
Interfacing 644
Interfacing memory cards 479
Interprocessor communications 59, 101, 119, 297, 301, 351
Interrupt controller 474
ISDN cards 167
iWarp multicomputer 59

Josephson junction devices 67
Josephson microprocessor 67

Logic Cell Arrays 211, 227, 291
Logic design 17

Machine control 517
Memory cards 479
Memory checking 599
Memory design 444
Message passing 101
Micro Channel bus 306
Microcode 21, 623
Microprocessor control 3, 177
Microprocessors
  68040 123
  80386 664
  Gmicro F32 series 589
  MC68030 653
Microprogramming 21
Modem 97
Modular hardware design 276
Modular software 39, 181
Modula-2 133, 145, 149, 153, 161, 167, 171, 177, 181, 186
MS-DOS 83, 427
Multiprocessors 3, 101, 171, 276, 297, 444, 457, 467, 491, 525, 573

Nubus 167

Object-oriented programming 149, 153
Occam 83, 606, 623, 631
Operating systems 186

Parallel algorithms 467
Parallel processing 3, 59, 119, 301
Partitioning software 181
PC applications 83, 427
PC boards 297
PC design 240
PLDs 240
Programming tools 10
Prolog logic programs 491
PS/2 microcomputers 306

RAM 599
Realtime control 644
Realtime imaging 107
Realtime programming 39, 177, 186, 606
Realtime systems 171, 276, 525
Register management 333
Relocating loader 427
Reprogrammable gate arrays 211, 227, 291
RISC microprocessors
  Am29000 397
  ARM 301
  ARM 3 407
  i860 89
  i960CA 385
  IDT 79R3001 435
  M88000 377
  R3000 367
  R6000 367
  RISC System/6000 357
  SPARC 417
RISC-like machines 231
Robotics 3, 276, 517, 565

Safety-critical systems 177, 318
Semicustom design 193, 211, 219, 227, 231, 240, 245, 251, 256, 531
Semicustom microprocessor 251
Sensors 74
Simulation 467
Smart sensors 74
Software fault tolerance 505
Software standards 133, 145
Speech recognition 267
State machine compiler 17
Switching networks 59

Test 245, 291, 444, 505, 525
Threaded register windows 333
Transputer networks 10, 267, 457, 497
Transputer systems 83, 351, 565
Transputers 606, 623, 631, 644
TRON 589

Verification techniques 623
VHDL 474
Video imaging 107
Vocoder algorithms 511
Vocoders 227

MICROPROCESSORS AND MICROSYSTEMS

Index to Volume 14 Numbers 1–10, pages 1–680 (1990)

Subject index

Abstract data types 153, 161
Adaptive equalizers 31
Apple Macintosh 167
ASICs 17, 219, 240, 256, 291, 341, 531
Assembler 177
Automotive applications 318

Binary-decision machines 231
Bus interfacing 306

Cache address comparators 653
Cache coherence 491
Cache memory 47, 61, 653, 664
Cache tag RAMs 47, 61, 664
CAD 17
Cellprocessors 119
Command language 517
Computer vision 573
Control applications 3, 219, 231
Custom microelectronics 197

Data communications 31
Data compression 555
Database machines 351
Decoders 256
Digital communications 97, 211, 256
Digital control 219, 231
Digital filters 31, 107, 555
Digital satellite modem 97
Digital signal processing 97, 497, 511, 555, 583
Digital signal processors
  TMS32020 511
  TMS320C25 97, 511
  Zoran Vector Signal Processor 555
Digital sound broadcasting 531
Distributed software 10

Embedded systems 171, 435
Emulation 291

Fault tolerance 599
FIFO links 297
Formal methods 318, 623, 631
Futurebus 491

Gate arrays 205, 240, 245, 256, 531
Graphics server 83

Hypercube multiprocessors 101

Image generation 341
Image processing 107, 267, 573, 583
Independent actuator 517
Inmos links 301, 644
Instruction sets 341, 631
Integrated sensors 74
Interfacing 644
Interfacing memory cards 479
Interprocessor communications 59, 101, 119, 297, 301, 351
Interrupt controller 474
ISDN cards 167
iWarp multicomputer 59

Josephson junction devices 67
Josephson microprocessor 67

Logic Cell Arrays 211, 227, 291
Logic design 17

Machine control 517
Memory cards 479
Memory checking 599
Memory design 444
Message passing 101
Micro Channel bus 306
Microcode 21, 623
Microprocessor control 3, 177
Microprocessors
  68040 123
  80386 664
  Gmicro F32 series 589
  MC68030 653
Microprogramming 21
Modem 97
Modular hardware design 276
Modular software 39, 181
Modula-2 133, 145, 149, 153, 161, 167, 171, 177, 181, 186
MS-DOS 83, 427
Multiprocessors 3, 101, 171, 276, 297, 444, 457, 467, 491, 525, 573

Nubus 167

Object-oriented programming 149, 153
Occam 83, 606, 623, 631
Operating systems 186

Parallel algorithms 467
Parallel processing 3, 59, 119, 301
Partitioning software 181
PC applications 83, 427
PC boards 297
PC design 240
PLDs 240
Programming tools 10
Prolog logic programs 491
PS/2 microcomputers 306

RAM 599
Realtime control 644
Realtime imaging 107
Realtime programming 39, 177, 186, 606
Realtime systems 171, 276, 525
Register management 333
Relocating loader 427
Reprogrammable gate arrays 211, 227, 291
RISC microprocessors
  Am29000 397
  ARM 301
  ARM 3 407
  i860 89
  i960CA 385
  IDT 79R3001 435
  M88000 377
  R3000 367
  R6000 367
  RISC System/6000 357
  SPARC 417
RISC-like machines 231
Robotics 3, 276, 517, 565

Safety-critical systems 177, 318
Semicustom design 193, 211, 219, 227, 231, 240, 245, 251, 256, 531
Semicustom microprocessor 251
Sensors 74
Simulation 467
Smart sensors 74
Software fault tolerance 505
Software standards 133, 145
Speech recognition 267
State machine compiler 17
Switching networks 59

Test 245, 291, 444, 505, 525
Threaded register windows 333
Transputer networks 10, 267, 457, 497
Transputer systems 83, 351, 565
Transputers 606, 623, 631, 644
TRON 589

Verification techniques 623
VHDL 474
Video imaging 107
Vocoder algorithms 511
Vocoders 227

Author index

Key: (AN) = application note; (U) = update

Admiraal, J C Distributed store allocation and file management for transputer networks 10
Alexiou, G see **Bourbakis, N G** 573
Allerton, D J see **Anido, M L** 341
Almaini, A E A ASIC implementation of a programmable error-trapping decoder for binary codes of length 15 256
Al-Khayatt, S S see **Cooling, J E** 171
Al-Mouhamed, M Multiprocessor system for realtime robotics applications 276
Anderson, A J Innovations-based signal generator for a transputer 497
Anido, M L RISC design for computer image generation 341
Arendt, D Realtime multiprogramming system for mine control centre 39
Ariff, S M see **Ghoshal, S K** 297
Aspnas, M Hathi-2 multiprocessor system 457
Aziz, S S see **Almaini, A E A** 256
Back, R J R see **Aspnas, M** 457
Blaquiere, Y VHDL design of a priority interrupt controller and synchronizer for the MC68008 474
Bourbakis, N G Multiprocessor vision system 573
Bourekas, P A IDT 79R3001 simplifies RISC-based control system 435
Bowen, J Formal specification of the ProCos/safemos instruction set 631
Carpenter, G F Mechanism for evaluating the effectiveness of software fault-tolerant structures 505
Catthoor, F see **van Sas, J** 444
Chester, E G Experiences of teaching semicustom design and testing 245
Clark, A P see **Marshall, W G** 97
Cooling, J E Software management in a Modula-2 environment for a multiprocessor, embedded system 171
Costa, L D F see **Sandler, M B** 583
Das, S R Design and implementation of a hypercube multiprocessor 101
Davidson, J see **Blaquiere, Y** 474
De Man, H see **van Sas, J** 444
de Vel, O Multitransputer architecture for the low-level characterization of speech spectrograms 267
Deas, M see **Duffell, J** 240
Dickson, J A Reprogrammable gate arrays for hardware accelerated IC design verification 291
Doar, G Interfacing considerations for Mitsubishi memory cards (AN) 479
Duffell, J ASICs in personal computers 240
Elbert, D see **Zales, S** 543
Eldon, J Using the TMC2301 image resampling sequencer (AN) 107
Everitt, D J Inexpensive performance using the Am29000 397
Ford, K see **Duffell, J** 240
Froitzheim, K see **Schulthess, P** 167
Fujimaki, N see **Hasuo, S** 67
Fujita, K see **Mori, S** 589
Fujita, M see **Sato, H** 17
Fulcher, J see **Milway, M** 644
Furber, S B ARM3 — a study in design for compatibility 407
Ghoshal, S K Simple low-cost multiprocessor based on message passing FIFO links 297
Goodeve, D M Communications coprocessor for the Acorn RISC machine 301
Gorton, I see **Milway, M** 644
Gough, M P see **Kuo, N H** 599
Groves, R D RISC System/6000 processor architecture 357
Guha, S see **Ghoshal, S K** 297
Hamied, K A Microprocessor implementation of adaptive equalizers 31
Harrold, S J Gate array implementation of a multichannel accumulator using a serial datapath 205
Hasuo, S Recent advances in Josephson junction devices 67
Hauptmann, S Portable Modula-2 based realtime operating system 186
Hayat, L see **Sandler, M B** 583
Heath, S Performance improvement techniques for the M88000 RISC architecture 377
Henry, R The state of the Modula-2 standard 133
Hoare, D W see **Marshall, W G** 97
Holmes, P G see **Patterson, E B** 219
Hurst, S L Custom microelectronics: the risk factor 197
Imamura, T see **Hasuo, S** 67
Inze, L see **van Sas, J** 444
Itoh, H see **Mori, S** 589
Kashara, H Parallel processing of robot arm dynamic control computation on multimicroprocessors 3
Khan, A CMOS and ECL implementation of MIPS RISC architecture 367
King, G A Vector signal processors and digital filters in data compression for electronic publishing 555
Kingswood, N see **Whitmarsh, W** 211
Kirk, B Understanding the art of modular engineering 181
Kirk, J see **Duffell, J** 240
Klaeren, H Embedding functionally described abstract data types into Modula-2 161
Klein, R-F see **Schulthess, P** 167
Kreulen, J T Application-dependent simulation of microprocessor-based multiprocessors 467
Kung, H T iWarp multicomputer with an embedded switching network (U) 59
Kuo, N H 3D parity checking models for errors in RAM memories used in space on-board computers 599
Lardennois, R Using Modula-2 for safety-critical control in an urban transportation system 177
Lehtinen, M Development of a command language for programming a robotic actuator 517
Li, Q RISC processors in a massively parallel database machine 351
Lioy, A Automatic compaction of microcode 21
Liu, B see **Dickson, J A** 291
Malen, T-E see **Aspnas, M** 457
Mammen, O Dynamic test bed for the on-board computer system of a space launch vehicle 525
Margulis, N i860 microprocessor internal architecture 89
Marriott, R Benefits of the reprogrammable gate array in a communications board 227
Marshall, W G Using DSPs to synchronize a digital satellite modem 97
Marvill, J see **Whitmarsh, W** 211
Mason, C see **Doar, G** 79
McGeady, S Inside Intel's i960CA superscalar processor 385
McLeod, R D see **Dickson, J A** 291
Mezzalama, M see **Lioy, A** 21
Miller, D R Exploiting large register sets 333
Milway, M Interfacing transputer links to external devices 644
Moir, T J see **Anderson, A J** 497
Mori, S Development of 32-bit microprocessor Gmicro F32 family products 589
Morley, D see **Patterson, E B** 219
Morris, A S Transputer application in robotics for dynamic modelling and path planning 565
Mulroy, T J Semicustom microprocessor: an undergraduate study 251
Nair, V V see **Mammen, O** 525
Odersky, M Extending Modula-2 for object-oriented programming 153
Oehler, R see **Groves, R D** 357
Packer, J Simpler realtime programming with the transputer (AN) 606
Papazoglou, M see **Bourbakis, N G** 573
Patnaik, L M see **Das, S R** 101
Patterson, E B Microprocessor/ASIC to total ASIC design for cycloconverter drives 219
Picton, P D see **King, G A** 555
Piguet, C Binary-decision and RISC-like machines for semicustom design 231
Pillay, K D A Relocating loader for MS-DOS .EXE executable files 427
Postol, M see **Arendt, D** 39
Pronk, C see **Admiraal, J C** 10
Ptak, A W see **Dickson, J A** 291
Puusaari, P see **Lehtinen, M** 517
Quammen, D J see **Miller, D R** 333
Rahman, M see **Hamied, K A** 31
Rajaraman, V see **Ghoshal, S K** 297
Reinig, G see **Hauptmann, S** 186
Rishe, N see **Li, Q** 351
Robinson, P IEEE Futurebus cache coherence protocol as a logic program 491
Rowe, J W K see **Mulroy, T J** 251
Safai, M Complete high-performance cache system for the 80386 (AN) 664
Sandler, M B Benchmarking processors for image processing 583
Sato, H Speed tunable finite state machine compiler: ZEPHCAD 17
Schulthess, P Access to an ISDN card from Modula-2 167
Shepherd, D Verified microcode design 623
Souter, J The position of Modula-2 among programming languages 145
Sreenath, D I see **Mammen, O** 525
Stodieck, R see **Safai, M** 664
Suehnel, C ASIC design: progress in the GDR 531
Sugiura, Y see **Sato, H** 17
Tal, D see **Li, Q** 351
Taylor, R W see **Goodeve, D M** 301
Thazhuthaveetil, M J see **Kreulen, J** 467
Thomas, A R P see **Furber, S B** 407
Thomas, P see **de Vel, O** 267
Tsakalos, N Autocorrelation-based pitch determination algorithms for realtime vocoders with the TMS32020/C25 511
Vaidya, N H see **Das, S R** 101
van Herwaarden, A W Introduction to sensors compatible with microprocessors 74
van Sas, J Testability strategy and test pattern generation for register files and customized memories 444
Vaughan, J MS-DOS memory resident transputer graphics server 83
Virvalo, T see **Lehtinen, M** 517
Wegner, R see **Eldon, J** 107
Whitmarsh, W Experience in using LCAs with a mobile communications design example 211
Wirth, N Modula-2 and object-oriented programming 149

Wolffenbuttel, R F see **van Herwaarden, A W** 74
Wyland, D C Cache tag RAM chips simplify cache memory design (AN) 47
Yates, R B see **Mulroy, T J** 251
Zales, S Intel flash EPROM for in-system reprogrammable nonvolatile storage (AN) 543
Zalzala, A M S see **Morris, A S** 565
Zelinsky, A see **Milway, M** 644
Zigouris, E see **Tsakalos, N** 511
Zomaya, A Y H see **Morris, A S** 565

Title index

Key: (AN) = application note; (U) = update

Access to an ISDN card from Modula-2 177
Application-dependent simulation of microprocessor-based multiprocessors 467
ARM3 — a study in design for compatibility 407
ASIC design: progress in the GDR 531
ASIC implementation of a programmable error-trapping decoder for binary codes of length 15 256
ASICs in personal computers 240
Autocorrelation-based pitch determination algorithms for realtime vocoders with the TMS32020/C25 511
Automatic compaction of microcode 21
Benchmarking processors for image processing 583
Benefits of the reprogrammable gate array in a communications board 227
Binary-decision and RISC-like machines for semicustom design 231
Cache tag RAM chips simplify cache memory design (AN) 47
CMOS and ECL implementation of MIPS RISC architecture 367
Communications coprocessor for the Acorn RISC machine 301
Complete high-performance cache system for the 80386 (AN) 664
Custom microelectronics: the risk factor 197
Design and implementation of a hypercube multiprocessor 101
Development of 32-bit microprocessor Gmicro F32 family products 589
Development of a command language for programming a robotic actuator 517
Distributed store allocation and file management for transputer networks 10
Dynamic test bed for the on-board computer system of a space launch vehicle 525
Embedding functionally described abstract data types into Modula-2 programs 161
Enhancing MC68030 microprocessor performance with the SN74ACT2155 cache (AN) 653
Experiences in using LCAs with a mobile communications design example 211
Experiences of teaching semicustom design and testing 245
Exploiting large register sets 333
Extending Modula-2 for object-oriented programming 153
Formal specification of the ProCos/safemos instruction set 631
Gate array implementation of a multichannel accumulator using a serial datapath 205
Hathi-2 multiprocessor system 457
Hungarian cellprocessor project (U) 119
i860 microprocessor internal architecture 89
IDT 79R3001 simplifies RISC-based control systems 435
IEEE Futurebus cache coherence protocol as a logic program 491
Inexpensive performance using the Am29000 397
Innovations-based signal generator for a transputer 497
Inside Intel's i960CA superscalar processor 385
Intel flash EPROM for in-system reprogrammable nonvolatile storage (AN) 543
Interfacing considerations for Mitsubishi memory cards (AN) 479
Interfacing transputer links to external devices 644
Introduction to sensors compatible with microprocessors 74
iWarp multicomputer with an embedded switching network (U) 59
Mechanism for evaluating the effectiveness of software fault-tolerant structures 505
Microprocessor implementation of adaptive equalizers 31
Microprocessor/ASIC to total ASIC design for cycloconverter drives 219
Modula-2 and object-oriented programming 149
MS-DOS resident transputer graphics server 83
Multiprocessor system for realtime robotics applications 276
Multiprocessor vision system 573
Multitransputer architecture for the low-level characterization of speech spectrograms 267
Parallel processing of robot arm dynamic control computation on multimicroprocessors 3
Performance improvement techniques for the M88000 RISC architecture 377
Portable Modula-2 based realtime operating system 186
Realtime multiprogramming system for mine control centre 39
Recent advances in Josephson junction devices 67
Relocating loader for MS-DOS .EXE executable files 427
Reprogrammable gate arrays for hardware accelerated IC design verification 291
RISC design for computer image generation 341
RISC processors in a massively parallel database machine 351
RISC System/6000 processor architecture 357
Safety-related design in microprocessor-based automotive applications (U) 318
Semicustom microprocessor: an undergraduate study 251
Simple low-cost multiprocessor based on message passing FIFO links 297
Simpler realtime programming with the transputer (AN) 606
Software management in a Modula-2 environment for a multiprocessor embedded system 171
SPARC: architecture to implementations (U) 417
Speed tunable finite state machine compiler: ZEPHCAD 17
Testability strategy and test pattern generation for register files and customized memories 444
The position of Modula-2 among programming languages 145
The state of the Modula-2 standard 133
Transputer applications in robotics for dynamic modelling and path planning 565
Understanding the art of modular engineering 181
User-configurable adapter interface chips for PS/2 Micro Channel (AN) 306
Using DSPs to synchronize a digital satellite modem 97
Using Modula-2 for safety-critical control in an urban transportation system 177
Using the TMC2301 image resampling sequencer 107
Vector signal processors and digital filters in data compression for electronic publishing 555
Verified microcode design 623
VHDL design of a priority interrupt controller and synchronizer for the MC68008 474
3D parity checking models for errors in RAM memories used in space on-board computers 599